

Customer No.: 31561  
Docket No.: 12530-US-PA  
Application No.: 10/709,006

**AMENDMENT**

Please amend the application as indicated hereafter.

**To the Specification**

Please added paragraph [0022.1] following paragraph [0022] as follows:

[0022.1] FIG. 4 is a flowing chart of a method of decompressing images using an image decompressing circuit according to a preferred embodiment of the present invention.

Please amend paragraph [0025] as follows:

[0025] FIG. 3 is a schematic diagram illustrating an image decompressing operation of an image decompressing circuit according to a preferred embodiment of the present invention. FIG. 4 is a flowing chart of a method of decompressing images using an image decompressing circuit according to a preferred embodiment of the present invention. As shown in ~~the diagram~~ FIGS. 3 and 4, the variable length decoding unit 210 of the image decompressing circuit 200 according to a preferred embodiment of the present invention uses a two-phase execution mechanism. Wherein, in the first phase, the variable length decoding unit 210 reads a compressed image picture and performs a debug analysis on it, however the data is not sent to the backend image picture recovery unit 220 for further processing, thus there is no pipeline process, and the system can operate in full speed. Meanwhile, the speed for the variable length decoding unit 210 to analyze an image picture is about 8 ~ 15 times faster than in the normal decoding operation, thus the object of accurately analyzing the images in high speed is achieved. Afterwards, an adequate subsequent mechanism is selected based on the analysis result.